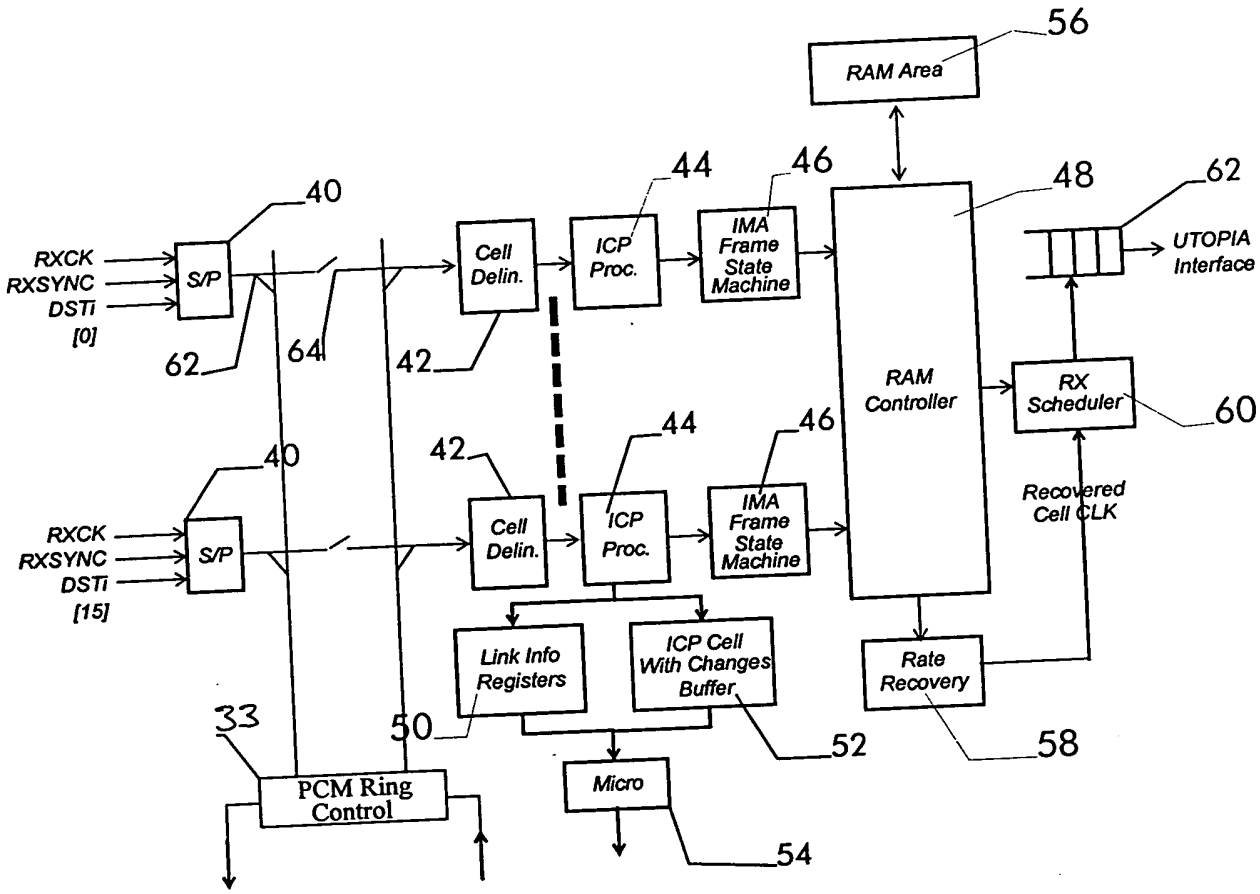
RAM Area
56
44
46
48
62
40
RXCK
RXSYNC
DSTi
[0]
S/P
Cell Delin.
ICP Proc.
IMA Frame State Machine
UTOPIA Interface
62
64
42
RAM Controller
RX Scheduler
60
40
42
44
46
RXCK
RXSYNC
DSTi
[15]
S/P
Cell Delin.
ICP Proc.
IMA Frame State Machine
Recovered Cell CLK
Link Info Registers
ICP Cell With Changes Buffer
Rate Recovery
33
50
52
58
PCM Ring Control
Micro
54

Figure 2

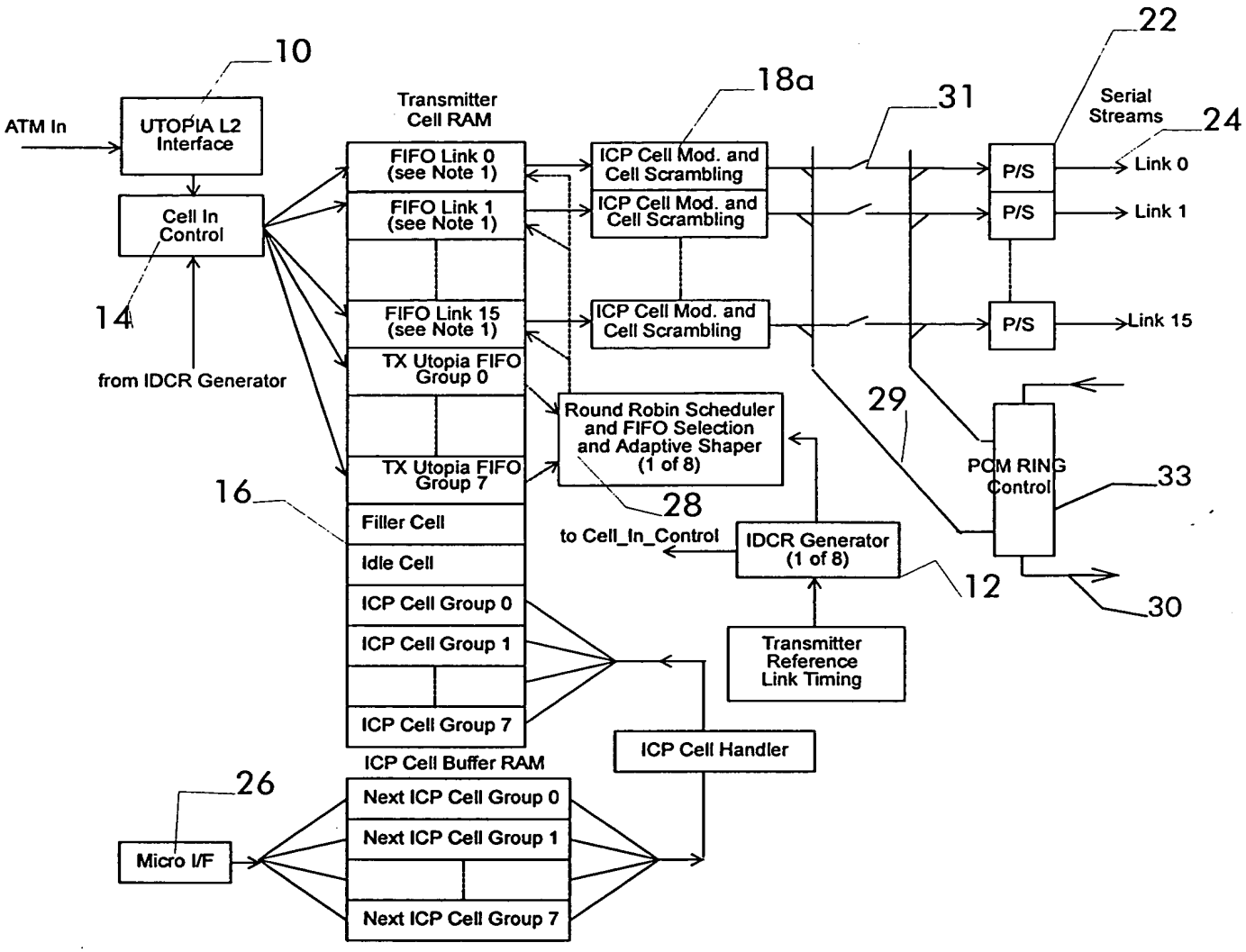

Figure 1

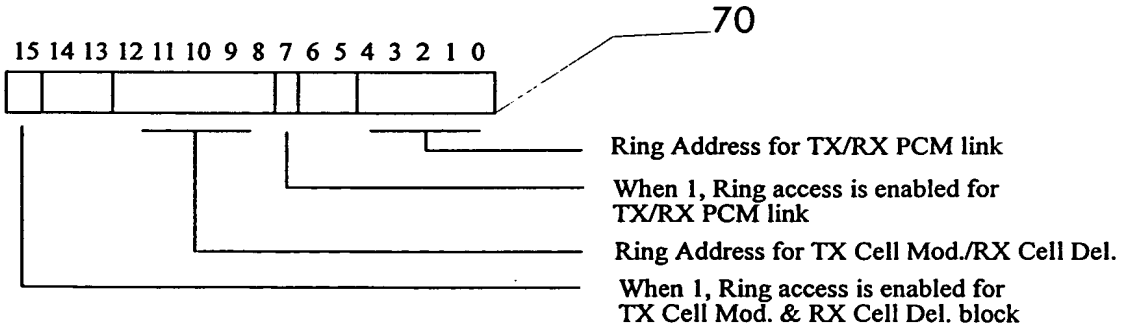

Figure 3

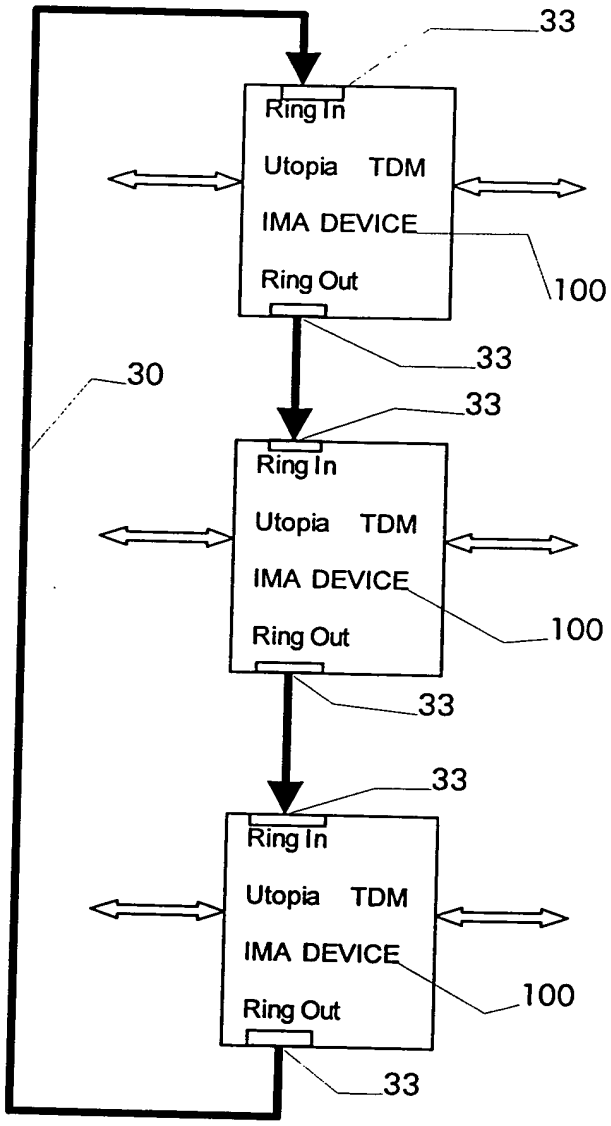
33
Ring In
Utopia TDM
IMA DEVICE
Ring Out
100
33
30
33
Ring In
Utopia TDM
IMA DEVICE
Ring Out
100
33
33
Ring In
Utopia TDM
IMA DEVICE
Ring Out
100
33

Figure 4

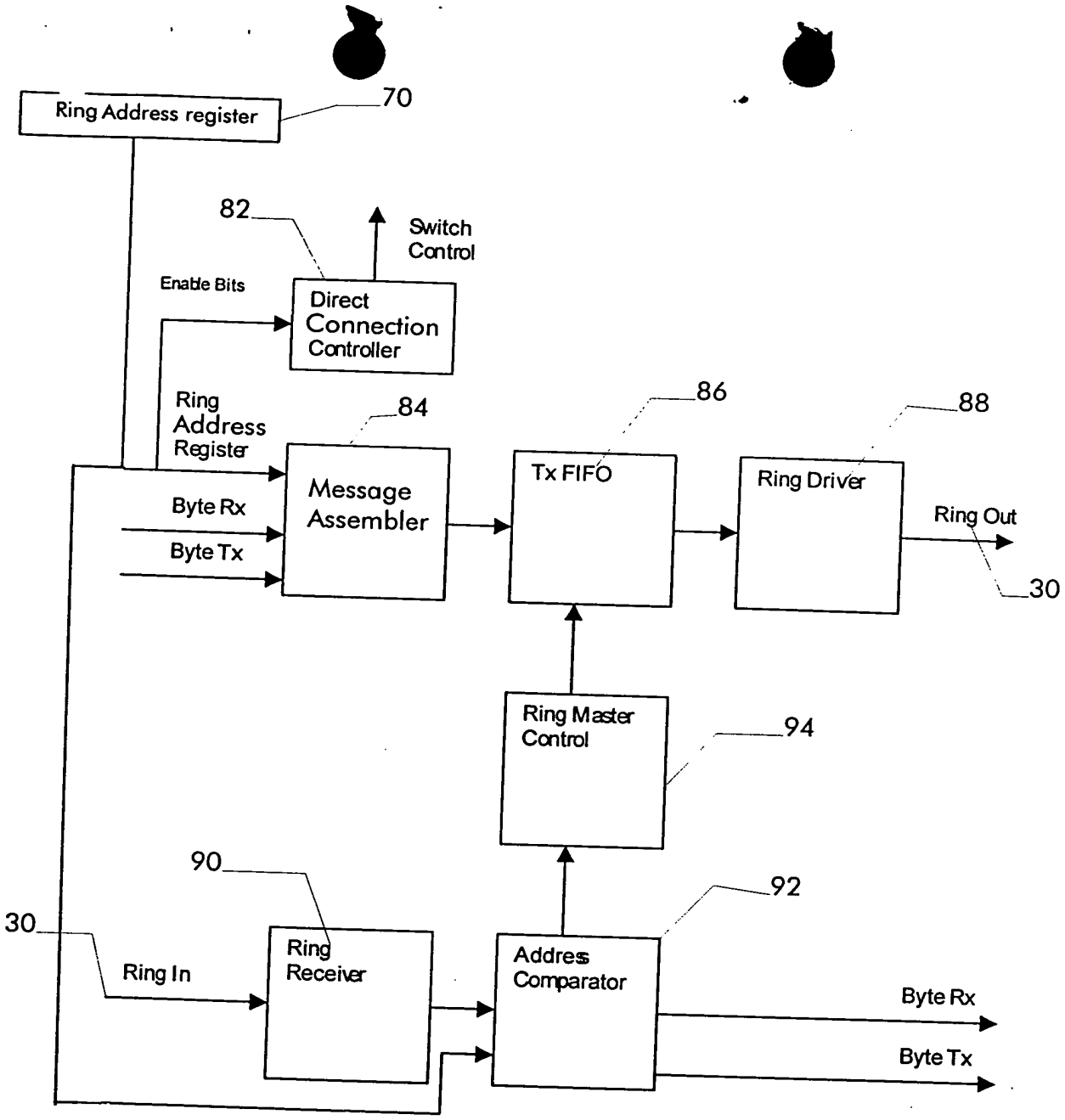
Ring Address register
70
82
Switch Control
Enable Bits
Direct Connection Controller
Ring Address Register
84
86
88
Message Assembler
Tx FIFO
Ring Driver
Byte Rx
Byte Tx
Ring Out
30
Ring Master Control
94
90
92
30
Ring In
Ring Receiver
Address Comparator
Byte Rx
Byte Tx

Figure 5